

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Li

Serial No. 09/670,874

Group Art Unit: 2823

Filed: September 27, 2000

Examiner: Kebede, Brook

For: Semiconductor Integrated Circuit Device

REQUEST FOR APPROVAL OF DRAWING CHANGES

Assistant Commissioner for Patents
Washington, D.C. 20231

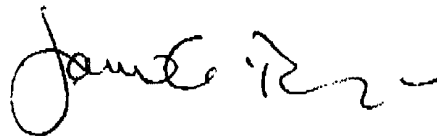
June 27, 2002

Dear Sir:

Adone
Please accept the corrections to Figs. 4 and 5 as indicated in red in the attached paper.

W
If any fees are required with respect to this paper, please charge Deposit Account No. 50-1770.

Respectfully submitted,



James A. Poulos, III
Attorney for Applicant
Registration No. 31,714

9001 Garland Ave.
Silver Spring, MD 20901
301 588-7013

Enclosures: Marked up Figs. 4 and 5
Docket No. '874

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TRANSMITTAL COVER PAGE

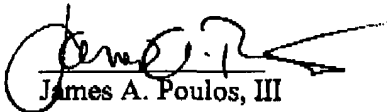
Page 1 of 15

Sir:

Transmitted with this cover page are the following documents in response to the restriction requirement mailed February 28, 2002.

Enclosures: EOT three months (2 originals)
Amendment (9 pages)
Abstract of the Disclosure (1 page)
Drawing changes (2 pages)

The undersigned certifies that this amendment was filed on June 27, 2002 by facsimile and transmitted to the USPTO to : 703-872-9318


James A. Poulos, III

Date: June 27, 2002

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JUN 28 2002

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